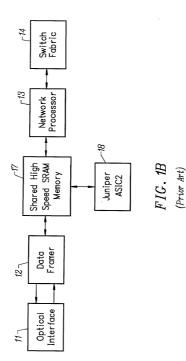


FIG. 1A (Prior Art)



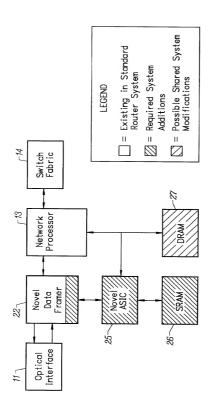


FIG. 2A

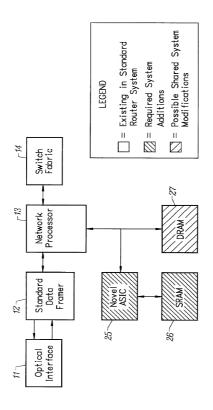
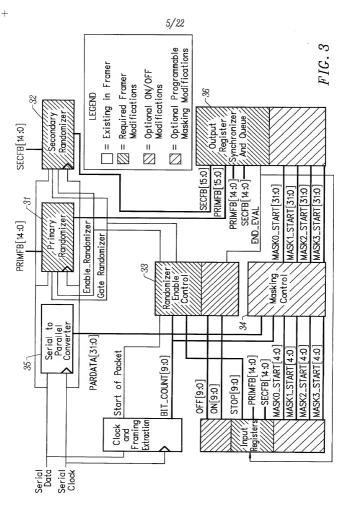
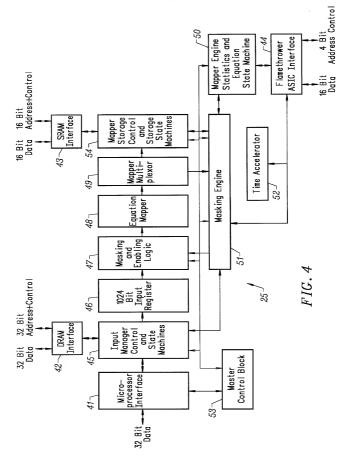
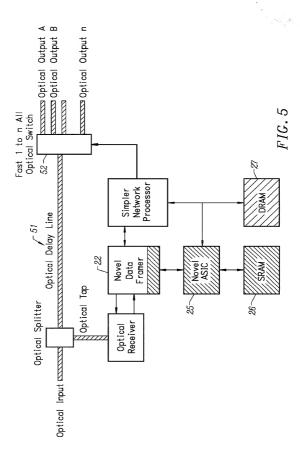


FIG. 2B







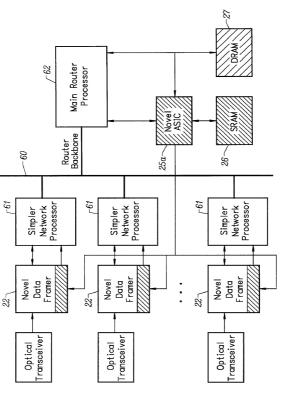
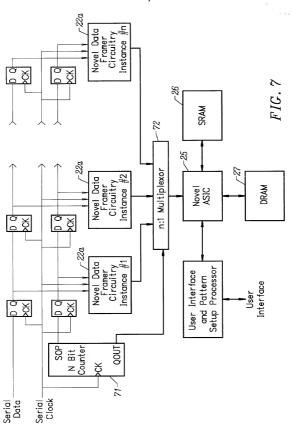
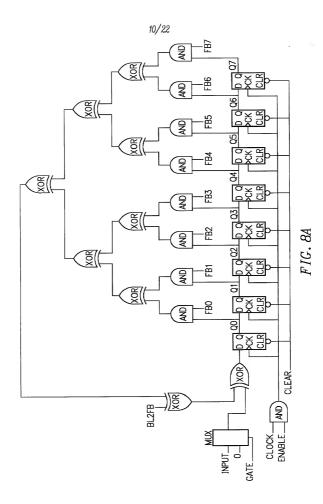
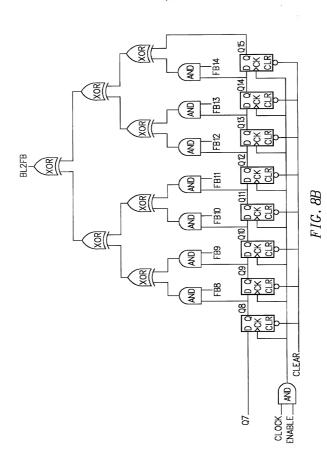
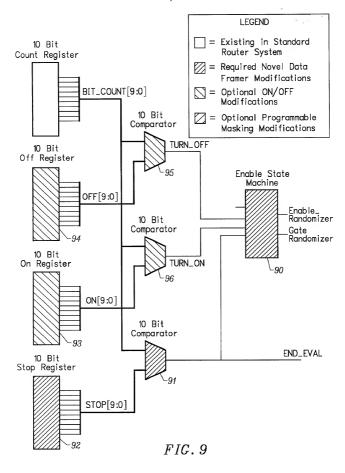


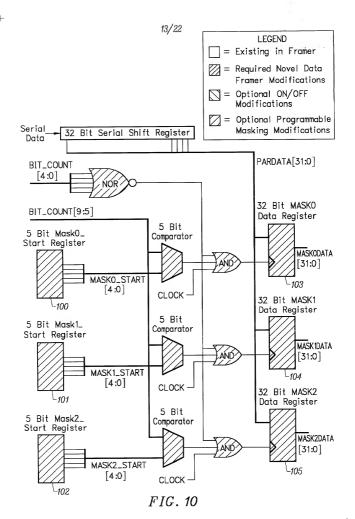
FIG. 6











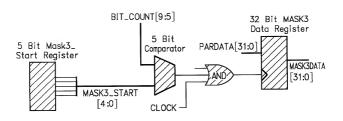


FIG. 11

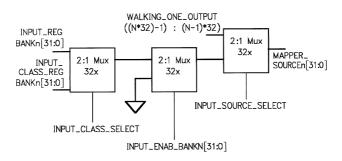


FIG. 12

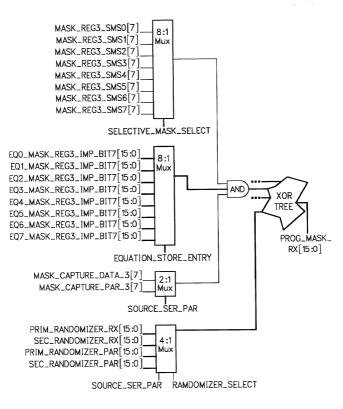


FIG. 13

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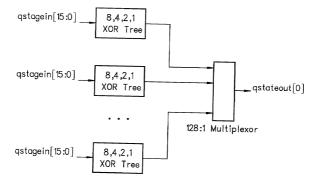


FIG. 14

TIME_ACC_CYC[0]

FIG. 15

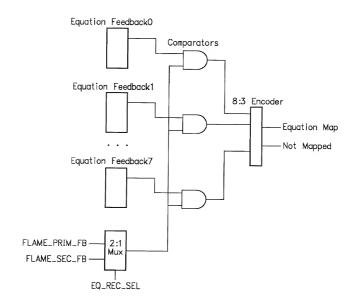


FIG. 16

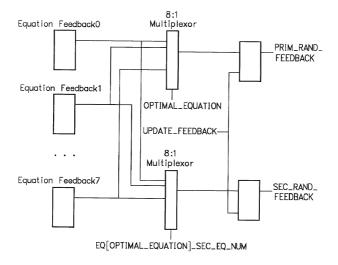
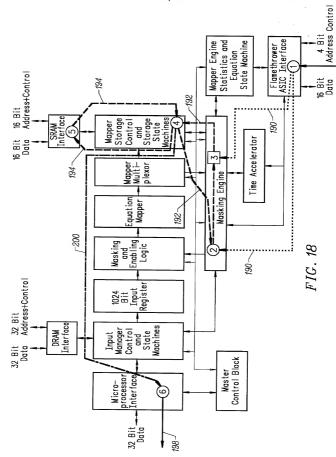
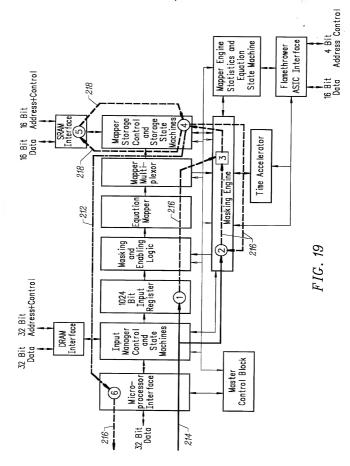


FIG. 17





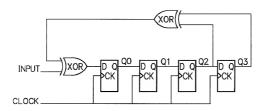


FIG. 20 (Prior Art)